

Form PTO-1449 (Modified) (Rev. 11/92)		U.S. Dept. of Commerce Patent and Trademark Office		ATTY. DOCKET NO. 254-094-CIP4-C1		SERIAL NO. 10/814,530	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)							
APPLICANT Russell Rapport, et al.				FILING DATE March 31, 2004			
GROUP 2822							

U.S. PATENT DOCUMENTS						
*Examiner Initial	Patent Number	Issue Date	Patentee	Class	Subclass	Filing Date (if appropriate)
TYT	US 5,117,282	05/26/1992	Salatino	357	81	10/29/1990
	US 5,448,511	09/05/1995	Paurus, et al.	365	52	06/01/1994
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FOREIGN PATENT OR PUBLISHED FOREIGN PATENT APPLICATION							
*Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation	
						Yes	No
TYT	2003309246(A)	10/31/2003	Japan	✓	—	Partial	
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	2003086760(A)	3/20/2003	Japan	—	—	Partial	
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	2001085592(A)	3/30/2001	Japan	—	—	Partial	
	2001077294(A)	3/23/2001	Japan	—	—	Partial	

OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)	
TYT	Chip Scale Review Online - An Independent Journal Dedicated to the Advancement of Chip-Scale Electronics. (Website 9 pages) Fjelstad, Joseph, Pacific Consultants L.L.C., Published Jan 2001 on Internet by U.S. Author Flexible Thinking: Examining the Flexible Circuit Tapes. (Website 2 pages) Fjelstad, Joseph., Published 4/20/2000 on Internet by U.S. Author Die Products: Ideal IC Packaging for Demanding Applications - Advanced packaging that's no bigger than the die itself brings together high performance and high reliability with small size and low cost. (Website 3 pages with 2 figures) Larry Gilg and Chris Windsor. December 23, 2002. Published on Internet

EXAMINER <u>Thanh Tran</u>	DATE CONSIDERED <u>5/27/05</u>
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*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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(37 CFR 1.98(b))									
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